

Day : Thursday
 Date: 4/14/2005
 Time: 10:01:06


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = WEBBER

First Name = THOMAS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07257853</u>	<u>4958146</u>	150	10/14/1988	MULTIPLEXOR IMPLEMENTATION FOR RASTER OPERATIONS INCLUDING FOREGROUND AND BACKGROUND COLORS	WEBBER, THOMAS
<u>07257985</u>	Not Issued	166	10/14/1988	METHOD AND APPARATUS FOR OPTIMIZING SELECTED RASTER OPERATIONS	WEBBER, THOMAS
<u>07258133</u>	<u>4908780</u>	150	10/14/1988	ANTI-ALIASING RASTER OPERATIONS UTILIZING SUB-PIXEL CROSSING INFORMATION TO CONTROL PIXEL SHADING	WEBBER, THOMAS
<u>07562429</u>	<u>5136524</u>	150	08/03/1990	MEHTOD AND APPARATUS FOR OPTIMIZING SELECTED RASTER OPERATIONS	WEBBER, THOMAS
<u>08049078</u>	<u>5539894</u>	150	04/20/1993	METHOD AND APPARATUS FOR OPTIMIZING A SECTOR CACHE TAG, BLOCK AND SUB-BLOCK STRUCTURE BASE ON MAIN MEMORY SIZE	WEBBER, THOMAS
<u>08269585</u>	Not Issued	166	07/01/1994	COMPUTER SYSTEM WITH A SHARED ADDRESS BUS AND PIPELINE WRITE OPERATIONS	WEBBER, THOMAS
<u>08705057</u>	<u>6141741</u>	150	08/29/1996	COMPUTER SYSTEM WITH A SHARED ADDRESS BUS AND PIPELINED WRITE OPERATIONS	WEBBER, THOMAS
<u>10311204</u>	Not Issued	030	06/09/2003	METHOD FOR CLEANING AND REGENERATING A PARTICLE FILTER IN A FILTER ASSEMBLY,	WEBBER, THOMAS

ASSEMBLY THEREFOR AND ELEMENTS FOR SAID ASSEMBLY					
09487529	Not Issued	041	01/19/2000	SYSTEM AND METHOD FOR ENHANCING COMMUNICATION BETWEEN DEVICES IN A COMPUTER SYSTEM	WEBBER, THOMAS P.
09513298	<u>6324601</u>	150	02/25/2000	SYSTEM FOR MANAGING MULTIPLE ORDERED SETS BY DEQUEUING SELECTED DATA PACKED FROM MEMORY STRUCTURE	WEBBER, THOMAS P.
09817509	<u>6820186</u>	150	03/26/2001	SYSTEM AND METHOD FOR BUILDING PACKETS	WEBBER, THOMAS P.
<u>09939973</u>	Not Issued	030	08/27/2001	PRECISE ERROR REPORTING	WEBBER, THOMAS P.
09949928	<u>6857030</u>	150	09/12/2001	METHODS, SYSTEM AND ARTICLE OF MANUFACTURE FOR PRE-FETCHING DESCRIPTORS	WEBBER, THOMAS P.
09995356	Not Issued	030	11/27/2001	METHOD AND SYSTEM FOR BUFFERING A DATA PACKET FOR TRANSMISSION TO A NETWORK	WEBBER, THOMAS P.
08225028	Not Issued	166	04/08/1994	METHOD AND APPARATUS FOR PROVIDING A COMPUTER WITH AN AC OUTLET WHICH TURNS OFF WHEN THE COMPUTER IS INACTIVE	WEBBER, THOMAS P.
08457555	<u>5687079</u>	150	06/01/1995	METHOD AND APPARATUS FOR IMPROVED CONTROL OF COMPUTER COOLING FAN SPEED	WEBBER, THOMAS P.
08821266	<u>5815023</u>	150	03/20/1997	UNBALANCED MULTIPLEXER AND ARBITER COMBINATION	WEBBER, THOMAS P.
08832000	Not Issued	161	02/20/1996	METHOD AND APPARATUS FOR PROVIDING A COMPUTER WITH AN AC OUTLET WHICH TURNS OFF WHEN THE COMPUTER IS INACTIVE	WEBBER, THOMAS P.
08884844	<u>606 772</u>	150	06/30/1997	SPLIT WRITE DATA	WEBBER,

				PROCESSING MECHANISM FOR MEMORY CONTROLLERS UTILIZING INACTIVE PERIODS DURING WRITE DATA PROCESSING FOR OTHER TRANSACTIONS	THOMAS P.
<u>08885010</u>	<u>6035348</u>	150	06/30/1997	METHOD FOR MANAGING MULTIPLE ORDERED SETS DEQUEUING SELECTED DATA PACKET FROM SINGLE MEMORY STRUCTURE	WEBBER, THOMAS P.
<u>09096450</u>	<u>6529518</u>	150	06/11/1998	METHOD AND APPARATUS FOR PROVIDING A NETWORK INTERFACE	WEBBER, THOMAS P.
<u>09108918</u>	<u>6347348</u>	150	06/30/1998	BUFFER MANAGEMENT SYSTEM HAVING AN OUPUT CONTROL CONFIGURED TO RETRIEVE DATA IN RESPONSE TO A RETRIEVAL REQUEST FROM A REQUESTING ONE OF A PLURALITY OF DESTINATIONS	WEBBER, THOMAS P.
<u>09478140</u>	<u>6880057</u>	150	01/05/2000	SPLIT WRITE DATA PROCESSING MECHANISM FOR MEMORY CONTROLLERS UTILIZING INACTIVE PERIODS DURING WRITE DATA PROCESSING FOR OTHER TRANSACTIONS	WEBBER, THOMAS P.
<u>09645864</u>	<u>6744765</u>	150	08/24/2000	MECHANISM FOR COMPLETING MESSAGES IN MEMORY	WEBBER, THOMAS P.
<u>09832681</u>	<u>6671690</u>	150	04/10/2001	DELETIONS ON CIRCULAR SINGLY LINKED LISTS	WEBBER, THOMAS PETER
<u>09891684</u>	<u>6883006</u>	150	06/25/2001	ADDITIONS ON CIRCULAR SINGLY LINKED LISTS	WEBBER, THOMAS PETER

Inventor Search Completed: No Records to Display.

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